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10th July 2024

17th July 2024

Notification of acceptance

24th July 2024

Deadline for paper upload

Technical Tracks

- Power electronic converters for ICT networks.
- Power systems for critical applications.
- Integrated operation and control of power converters for ICT infrastructures.
- Energy storage for communications systems.
- Resilient, reliable, and highly available power infrastructure for mission-critical applications.
- Infrastructure for communications systems.
- Critical power for electric transportation.

Please visit www.intelec2024.org/call-for-papers.php for sub-track details

Invited Speakers

Srinivas K IIT Bhubaneswar	Sandeep Anand IIT Mumbai	Kamalesh Hatua IIT Madras	Moumita Das IIT Mandi	Pallavi Bharadwaj IIT Gandhinagar	Shiladri Chakraborty IIT Mumbai
Soumya Subhra Nag IIT Delhi	Sumit Pramanick IIT Delhi	Rajeev Kumar Singh IIT(BHU) Varanasi	Chandan Kumar IIT Gauhati	N Lakshminarasamma IIT Madras	Venugopal Subramaniam Ametek Programmable Power

Plenary Speakers

Fei "Fred" Wang University of Tennessee	Subhashish Bhattacharya North Carolina State University	Homer Alan Mantooth University of Arkansas Fayetteville	Kaushik Rajasekhar University of Houston	Alexander Tessarolo Texas Instruments	Brij Singh John Deere
Pat Wheeler University of Nottingham	Shashidhar M Delta Electronics	Prasad Enjeti Texas A&M University	Srinivasan Raghavan IISc India	Vinay Shenoy Infineon Technologies Semiconductor, India	

Tutorial Speakers

Santanu Kapat IIT Kharagpur	Sairaj Dhople University of Minnesota	G Narayanan IISc Bengaluru	Supratim Basu Bose Research	L Umanand IISc Bengaluru	Shamibrota Kishore Roy Delta Electronics India
Bhim Singh IIT Delhi	Harish Sarma Krishnamoorthy University of Houston				

Registration Category	Early Bird	
Academic & Non Academic	USD*	INR*
PELS Member	350	29750

Above mentioned rates are exclusive of 18% GST

Papers presented at the conference will be published in IEEE Xplore.

The authors of selected papers will be encouraged to submit an extended version for publishing in journals of the IEEE Power Electronics Society.

• 50% discount for academic participants from countries with HDI < 0.7 (India, Srilanka etc.)

• 30% discount for non academic participants from countries with HDI < 0.7 (India, Srilanka etc.)

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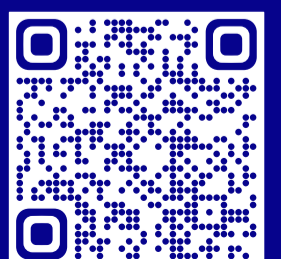
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